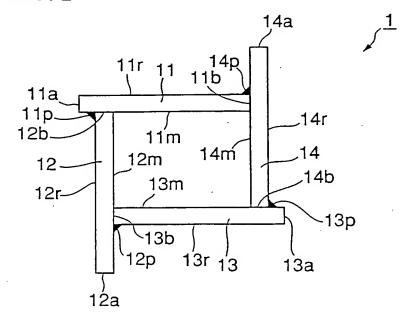
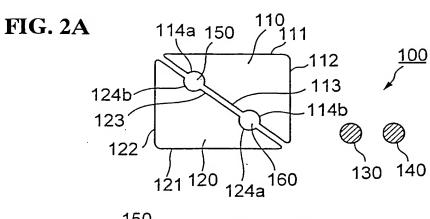
FIG. 1







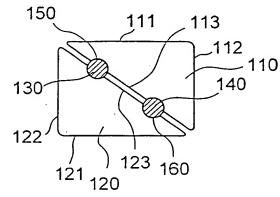
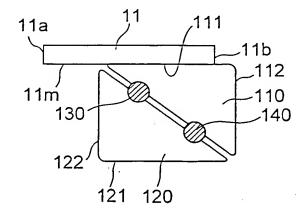


FIG. 2C





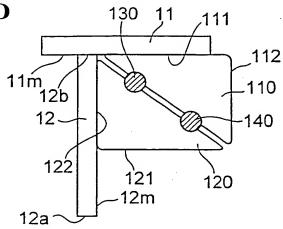


FIG. 3E

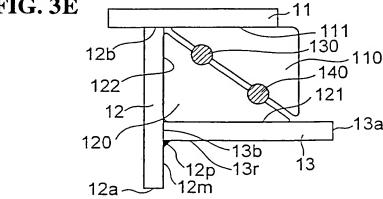
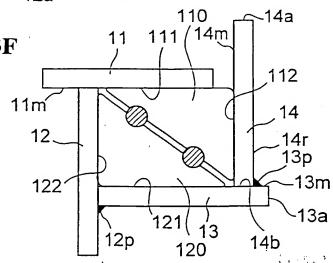
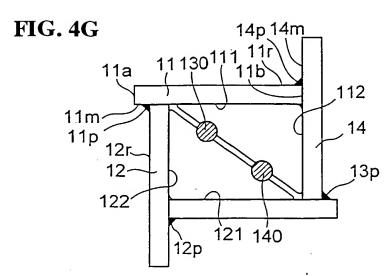
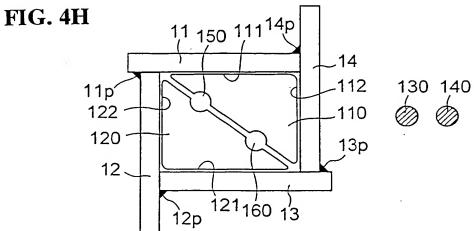


FIG. 3F







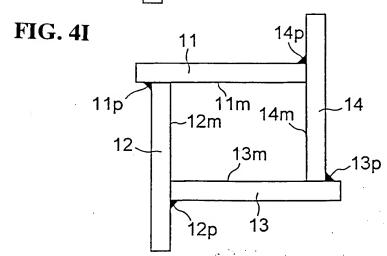


FIG. 5A

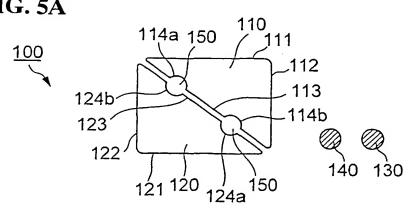


FIG. 5B

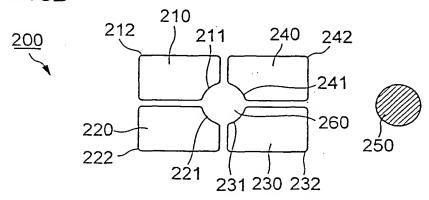


FIG. 5C

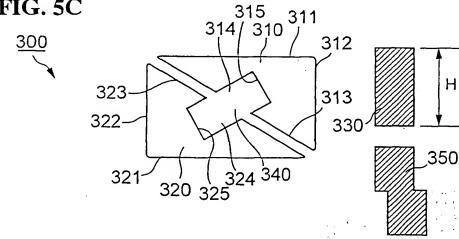


FIG. 6 IMAGE INPUT 41 MICRO-PROCESSORE FRAME MEMORY 40 43 CONTROL CIRCUIT FOR DRIVING LIGHT MODULATING DEVICE TIMING GENERATOR 42 35 33 32 31h 31m, 30 31

FIG. 7

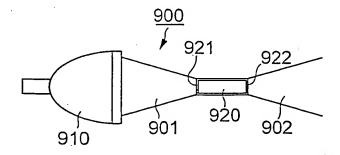


FIG. 8

